

8 GHz to 8.3 GHz, Dielectric Resonator Oscillator Module

Data Sheet

FEATURES

Frequency range: 8.0 GHz to 8.3 GHz Low SSB phase noise center frequency: -122 dBc/Hz at 10 kHz offset Voltage supply: 6 V to 15 V at a 116 mA supply current Internal voltage regulator Internal buffer amplifier High power output: 13.5 dBm -40°C to +85°C operating temperature range

APPLICATIONS

Test and measurement equipment Lab instrumentation Industrial and medical equipment Military, electronic warfare (EW), electronic counter measures (ECM) communications

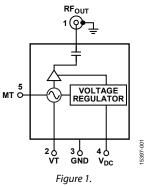
GENERAL DESCRIPTION

The HMC-C200 is a high performance dielectric resonator oscillator (DRO) module that incorporates Analog Devices, Inc., ultralow phase noise technology and provides -122 dBc/Hz single sideband (SSB) phase noise at a 10 kHz offset. The output buffer also provides 13.5 dBm of power output. Internal temperature compensation allows this DRO to operate over a temperature range of -40° C to $+85^{\circ}$ C with a frequency drift rate of only 2 ppm/°C. The VT port accepts an analog tuning voltage from 2 V to 12 V and provides an electric tuning range of ± 1 MHz from the center frequency.

The DRO is packaged in a small, moisture sealed 1.5 inch \times 1.5 inch (36 mm \times 36 mm) module with a field replaceable subminiature version A (SMA) connector. The HMC-C200 can be used as a drop in module if the SMA connector is not used. The HMC-C200 is available from 8.0 GHz to 8.3 GHz in 25 MHz frequency steps.

HMC-C200

FUNCTIONAL BLOCK DIAGRAM



Rev. F

Document Feedback

Information furnished by Analog Devices is believed to be accurate and reliable. However, no responsibility is assumed by Analog Devices for its use, nor for any infringements of patents or other rights of third parties that may result from its use. Specifications subject to change my No license is granted by implication or otherwise under any patent or patent rights of Analog Devices. Trademarks and registered trademarks are the property of their respective owners.

One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106, U.S.A. Tel: 781.329.4700 ©2017 Analog Devices, Inc. All rights reserved. Technical Support www.analog.com

TABLE OF CONTENTS

Features	1
Applications	1
Functional Block Diagram	1
General Description	1
Revision History	2
Specifications	3
Electrical Specifications	3
Absolute Maximum Ratings	4
ESD Caution	4

REVISION HISTORY

This Hittite Microwave Products data sheet has been reformatted to meet the styles and standards of Analog Devices, Inc.

3/2017—v06.0612 to Rev. F

Updated Format Univer	sal
Changes to Features Section and General Description Section	1
Changes to Table 1	3
Changes to Endnote 1, Table 2	4
Changes to Table 3	. 5
Changed Application Notes Section to Theory of Operation	
Section	7
Changes to DRO Tuning Procedure Section	7
Added Application Circuits Section	8
Updated Outline Dimensions	9
Changes to Ordering Guide	9

Pin Configuration and Function Descriptions	5
Interface Schematics	5
Typical Performance Characteristics	6
Theory of Operation	7
DRO Tuning Procedure	7
Application Circuits	8
Outline Dimensions	9
Ordering Guide	9

SPECIFICATIONS ELECTRICAL SPECIFICATIONS

 $T_A = 25^{\circ}$ C, frequency = 8.2 GHz, $V_{DC} = 6.5$ V, unless otherwise noted.

Table 1.

Parameter	Min	Тур	Max	Units	Test Conditions/Comments
FREQUENCY					
Range	8.0		8.3	GHz	See the Ordering Guide for available frequencies
Accuracy	-250		+250	kHz	
Pushing		5		kHz/V	
Pulling		5		kHz p-p	Into 2:1 voltage standing wave ratio (VSWR)
Drift Rate		2		ppm/°C	-40°C to +85°C
Drift vs. Time		1		ppm/Year	At 25°C
POWER OUTPUT	12	13.5		dBm	
SSB PHASE NOISE, CENTER FREQUENCY					
At 1 kHz Offset		-95		dBc/Hz	
At 10 kHz Offset	-117	-122		dBc/Hz	
At 100 kHz Offset		-140		dBc/Hz	
At 1 MHz Offset		-150		dBc/Hz	
TUNING VOLTAGE (VT)	2		12	V	
ELECTRIC TUNING RANGE	-1		+1	MHz	
HARMONIC DISTORTION					
Second, 2 four		-28		dBc	
Third, 3 four		-35		dBc	
OUTPUT RETURN LOSS		12		dB	
VOLTAGE SUPPLY (V _{DC})	6		15	V	
SUPPLY CURRENT		116		mA	$V_{DC} = 6.5 V$

ABSOLUTE MAXIMUM RATINGS

Table 2.

Parameter	Rating
V _{DC}	15 V
VT	0 V to 15 V
Storage Temperature Range	–65°C to +150°C
Operating Temperature Range	-40°C to +85°C1

¹ While the HMC-C200 operates over this temperature range, it is not guaranteed that the specific frequency is in the tuning range at all temperatures. For additional options, contact a local Analog Devices sales or distribution representative for information on additional screening to ensure any required operation over temperature.

Stresses at or above those listed under Absolute Maximum Ratings may cause permanent damage to the product. This is a stress rating only; functional operation of the product at these or any other conditions above those indicated in the operational section of this specification is not implied. Operation beyond the maximum operating conditions for extended periods may affect product reliability.

ESD CAUTION



ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

PIN CONFIGURATION AND FUNCTION DESCRIPTIONS

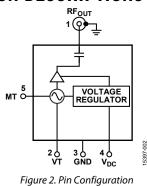
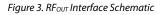


Table 3. Pin Function Descriptions

Pin No.	Mnemonic	Description
1	RFout	Radio Frequency Output. RF _{OUT} is ac-coupled and uses a female SMA connector. See Figure 3 for the interface schematic.
2	VT	Tuning Voltage. Control voltage and modulation input. Modulation bandwidth is 5 kHz for an ideal 2 V to 12 V source. See Figure 4 for the interface schematic.
3	GND	Ground. Must be connected to power supply ground. See Figure 5 for the interface schematic.
4	V _{DC}	Positive Power Supply Voltage. V_{DC} = 6 V to 15 V. See Figure 6 for the interface schematic.
5	MT	Mechanical Tuning Screw. MT is set at the factory per the specified frequency of the customer. See Figure 7 for the interface schematic.

INTERFACE SCHEMATICS

RFOUT OH 15397-003 0-Ţ



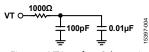


Figure 4. VT Interface Schematic



Figure 5. GND Interface Schematic

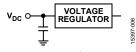






Figure 7. MT Interface Schematic

HMC-C200

TYPICAL PERFORMANCE CHARACTERISTICS

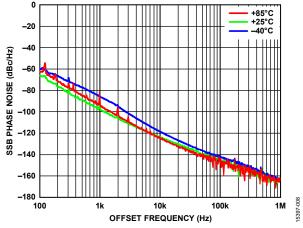
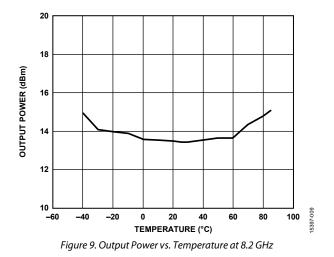


Figure 8. SSB Phase Noise vs. Offset Frequency for Various Temperatures at 8.2 GHz



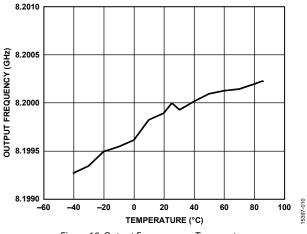
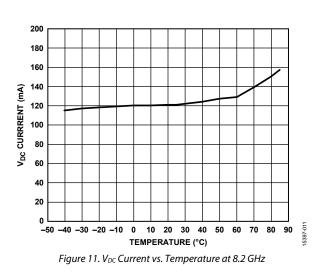


Figure 10. Output Frequency vs. Temperature



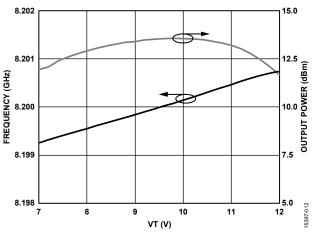
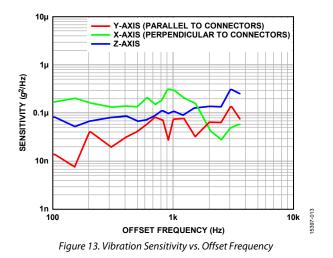


Figure 12. Frequency and Output Power vs. Tuning Voltage(VT)



THEORY OF OPERATION DRO TUNING PROCEDURE

The DRO turning procedure is as follows:

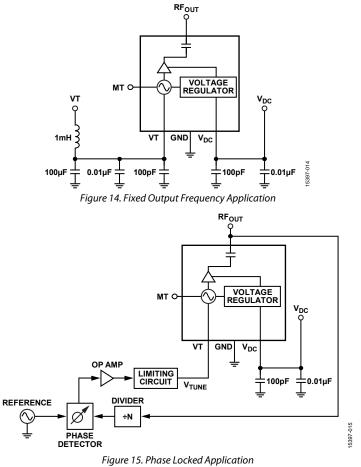
- A mechanical tuning (MT) screw is provided on the HMC-C200, which can adjust the center frequency by approximately ±20 MHz from the factory setting. This screw is set at the factory per the specified frequency of the customer, as outlined in the Ordering Guide section. Adjusting the screw clockwise increases the frequency, while turning the screw counterclockwise decreases the frequency. The MT screw has stops at either end of its approximately 8 turn range. Do not attempt to force the screw past the stops.
- 2. Connect the RF output to a spectrum analyzer and adjust the MT screw to set the output frequency within 1 MHz of the desired value.
- 3. Set the spectrum analyzer settings for a 10 MHz span centered on the desired output frequency. Change the reference value of the spectrum analyzer approximately 15 dBm and change the vertical scale to 1 dB per division so the peak oscillation frequency is visible on the screen.
- 4. Use the tuning voltage (VT) on Pin 2 to maximize the DRO output power and note the frequency. The tuning voltage must not be adjusted higher than 15 V.
- 5. If the frequency is meeting the new requirement and the output power is greater than 12 dBm, the tuning procedure is complete. If the frequency is not meeting the requirement, carefully readjust the MT screw and VT until the output frequency is within the required range.

The VT sustains oscillation over a fairly narrow range (approximately 1 MHz) before output power, temperature stability, and phase noise performance starts to degrade. In some cases, it is possible for the VT to be adjusted to the point where oscillation stops. Therefore, use the MT screw as a coarse tune, and use the VT effectively as a fine tune. In phase-locked loop (PLL) applications where the VT is used within the loop, this limited voltage range must be considered during the design of the loop. Use limiting circuit must be used to keep the VT voltage within the optimal range.

If the output power is less than 12 dBm, the DRO may be at the correct frequency; however, the phase noise performance and stability over temperature may degrade. It is recommended that the customer verify phase noise and stability after custom tuning.

HMC-C200

APPLICATION CIRCUITS



OUTLINE DIMENSIONS

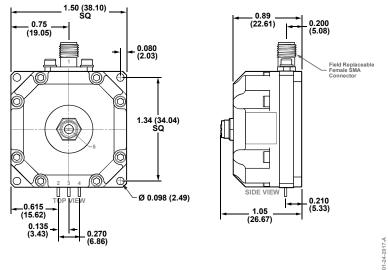


Figure 16. 5-Lead Connectorized Hermetic Module [MODULE] (ML-5-3) Dimensions shown in inches and (millimeters)

ORDERING GUIDE

Model	Temperature Range	Module Description	Package Option
HMC-C200-8000	-40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8025	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8050	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8075	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8100	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8125	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8150	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8175	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8200	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8225	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8250	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8275	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3
HMC-C200-8300	–40°C to +85°C	5-Lead Connectorized Hermetic Module [MODULE]	ML-5-3



www.analog.com

©2017 Analog Devices, Inc. All rights reserved. Trademarks and registered trademarks are the property of their respective owners. D15397-0-3/17(F)

Rev. F | Page 9 of 9